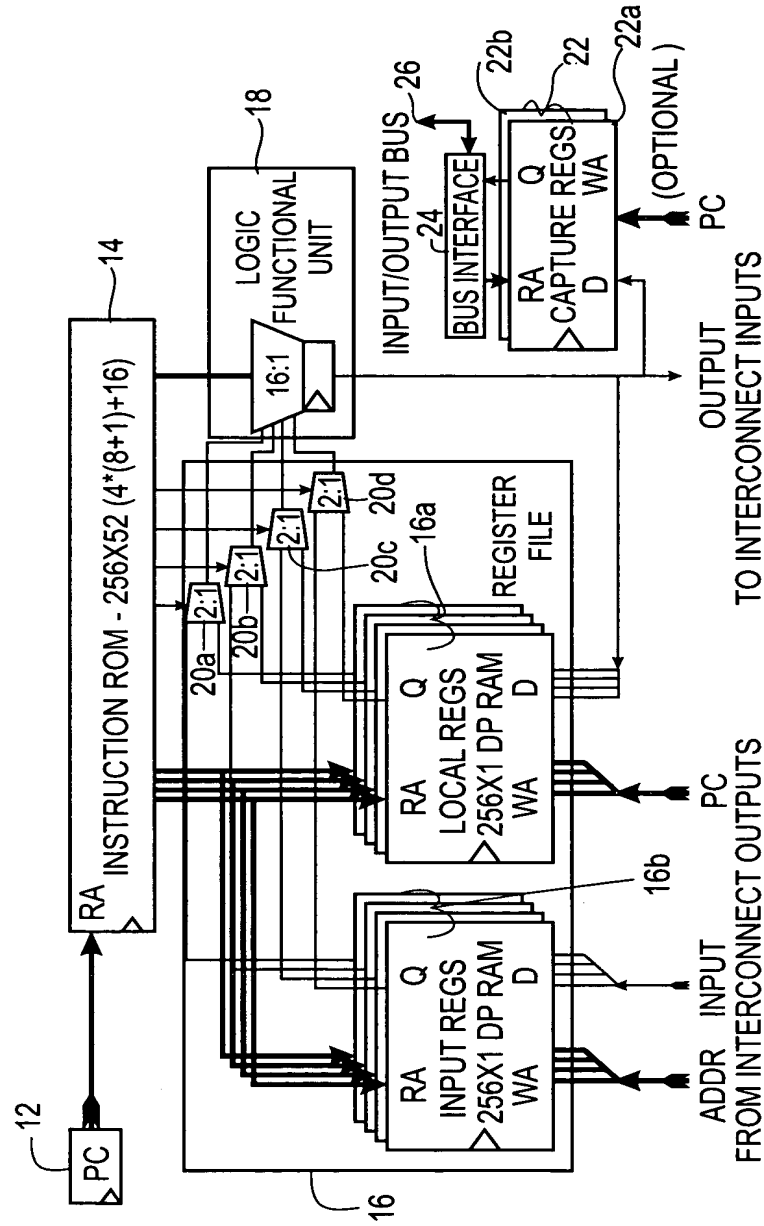




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 S. No. 10/669,095

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Logic Processor for programming into FPGAs

(All memories and registers are synchronously clocked by the common system clock.)

FIG. 1

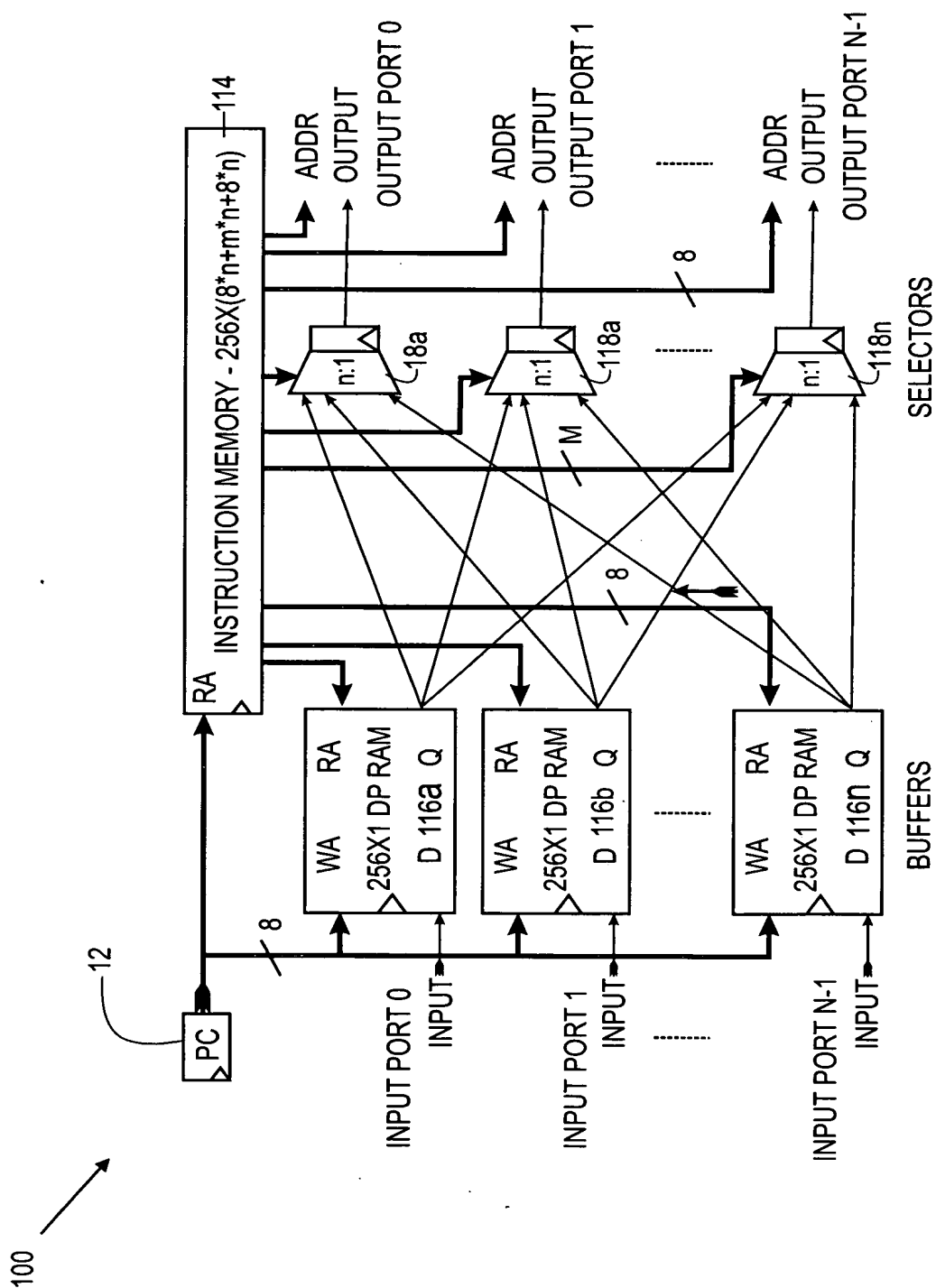
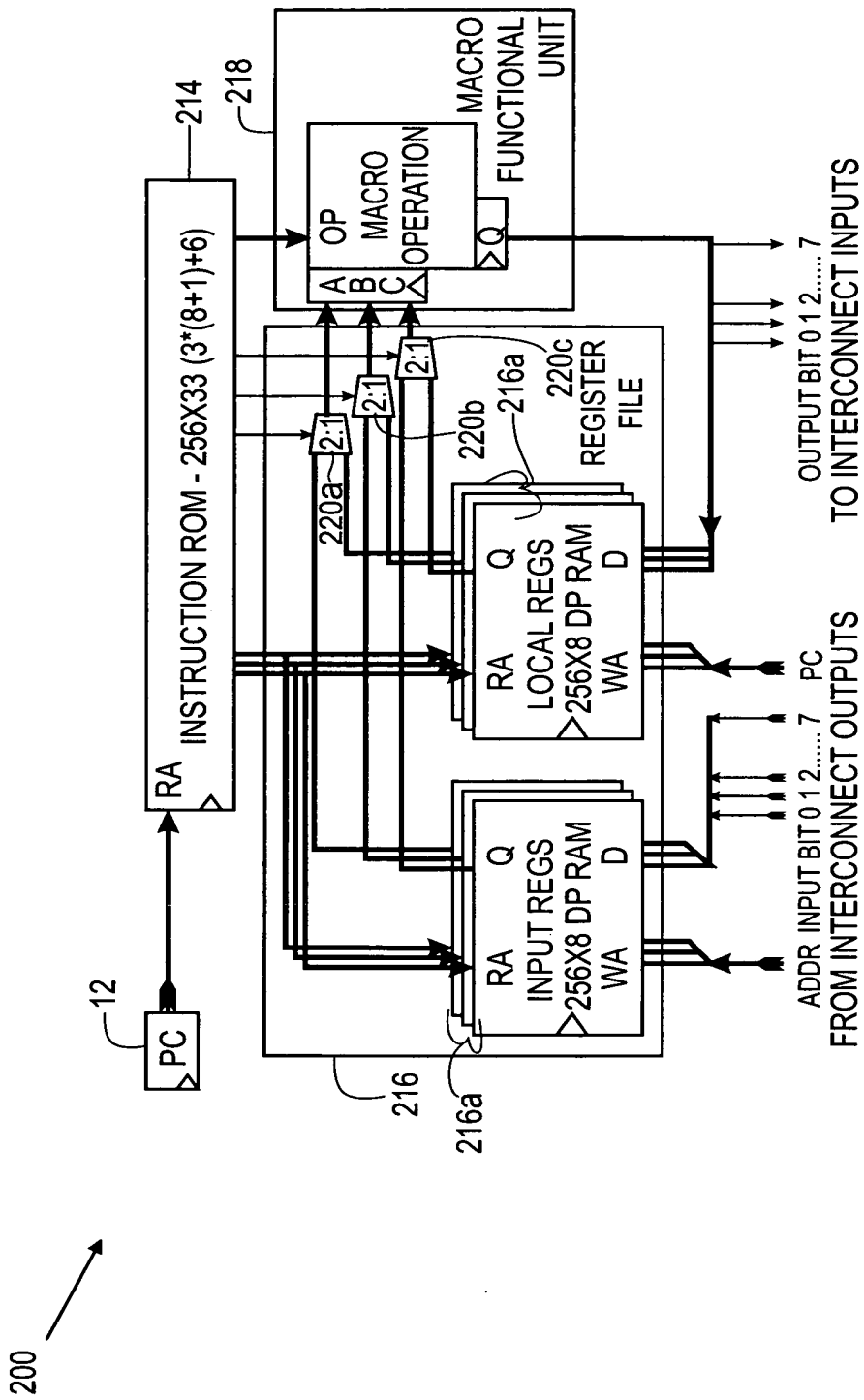
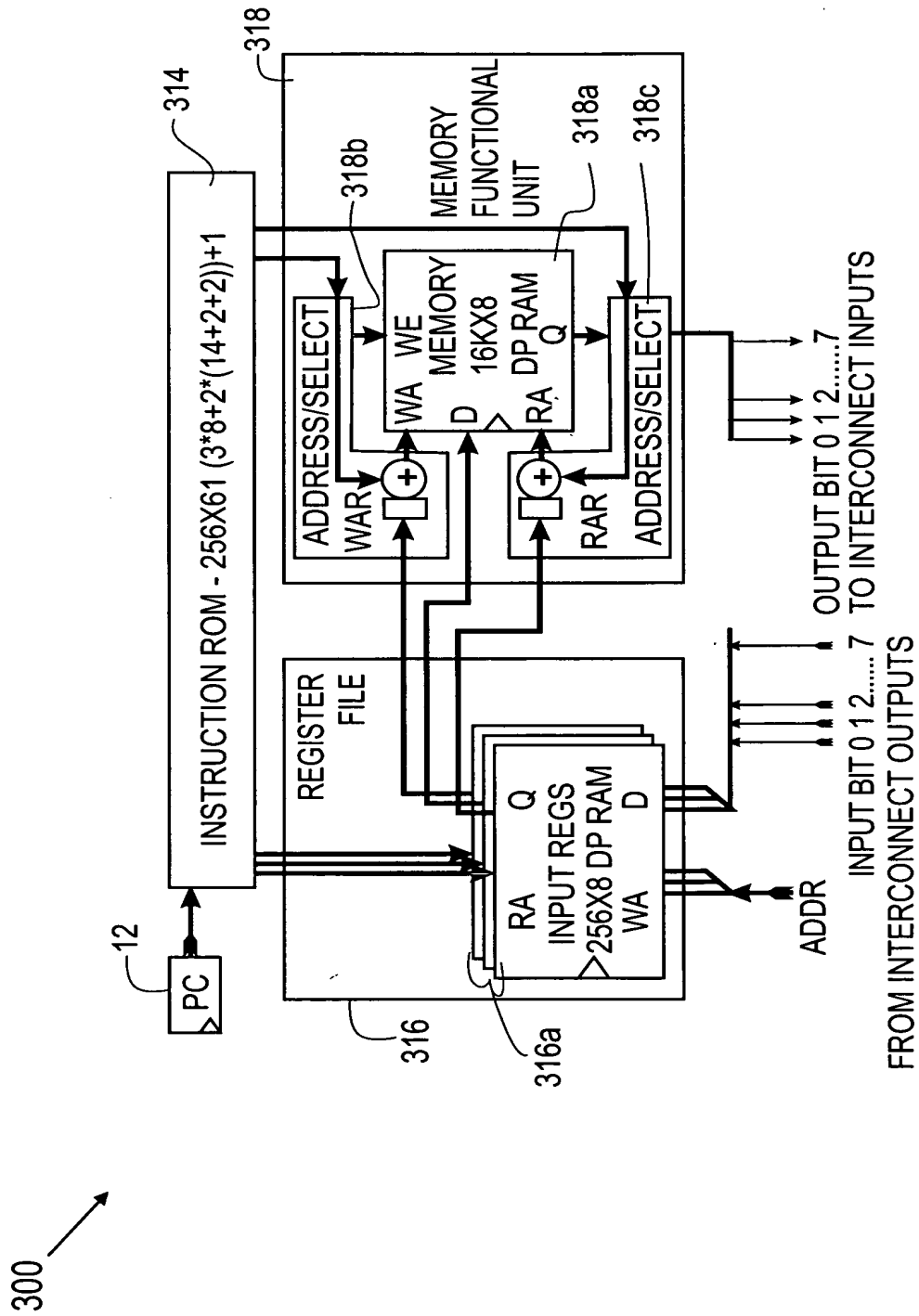


FIG. 2



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MEMORY PROCESSOR

FIG. 4

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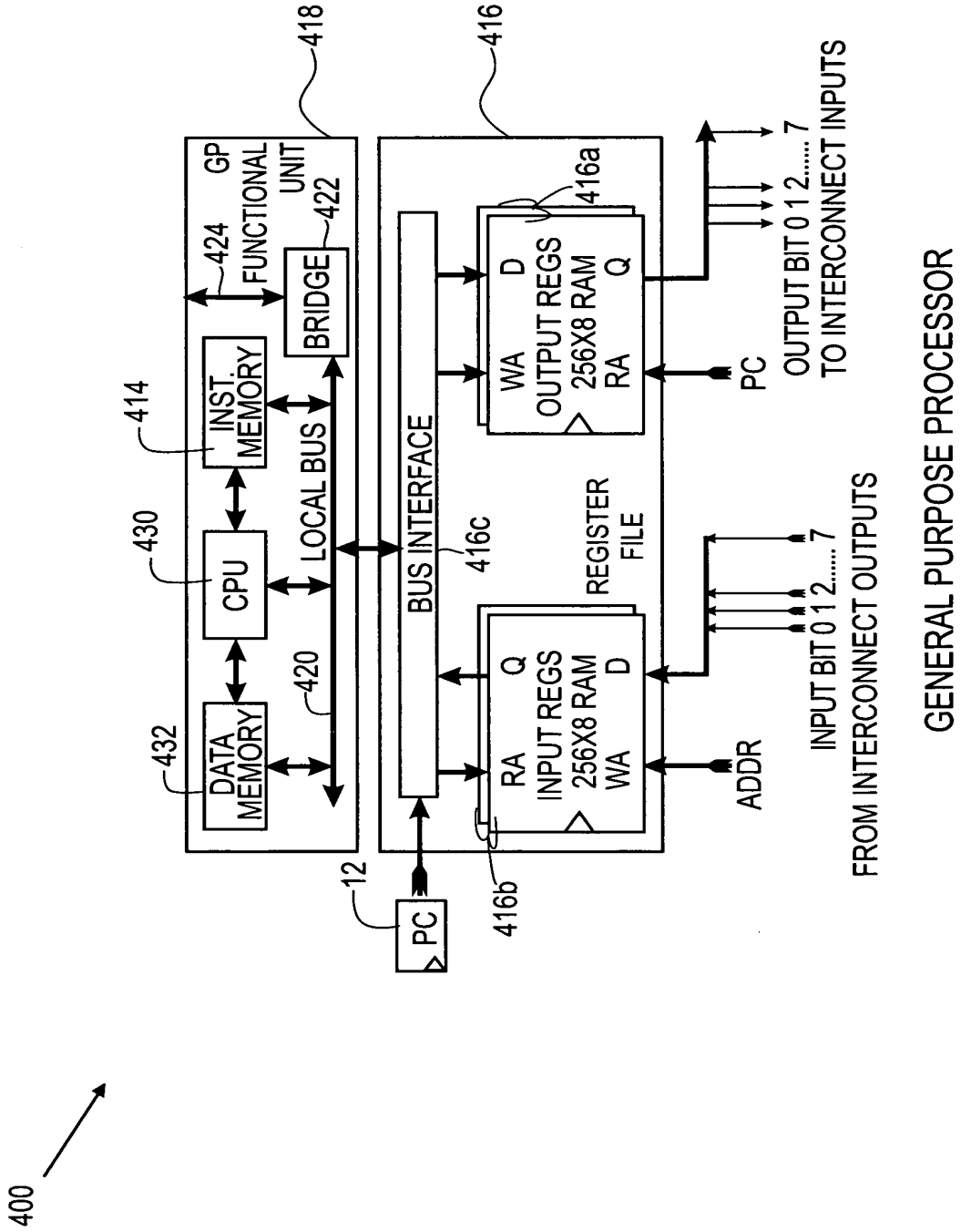


FIG. 5

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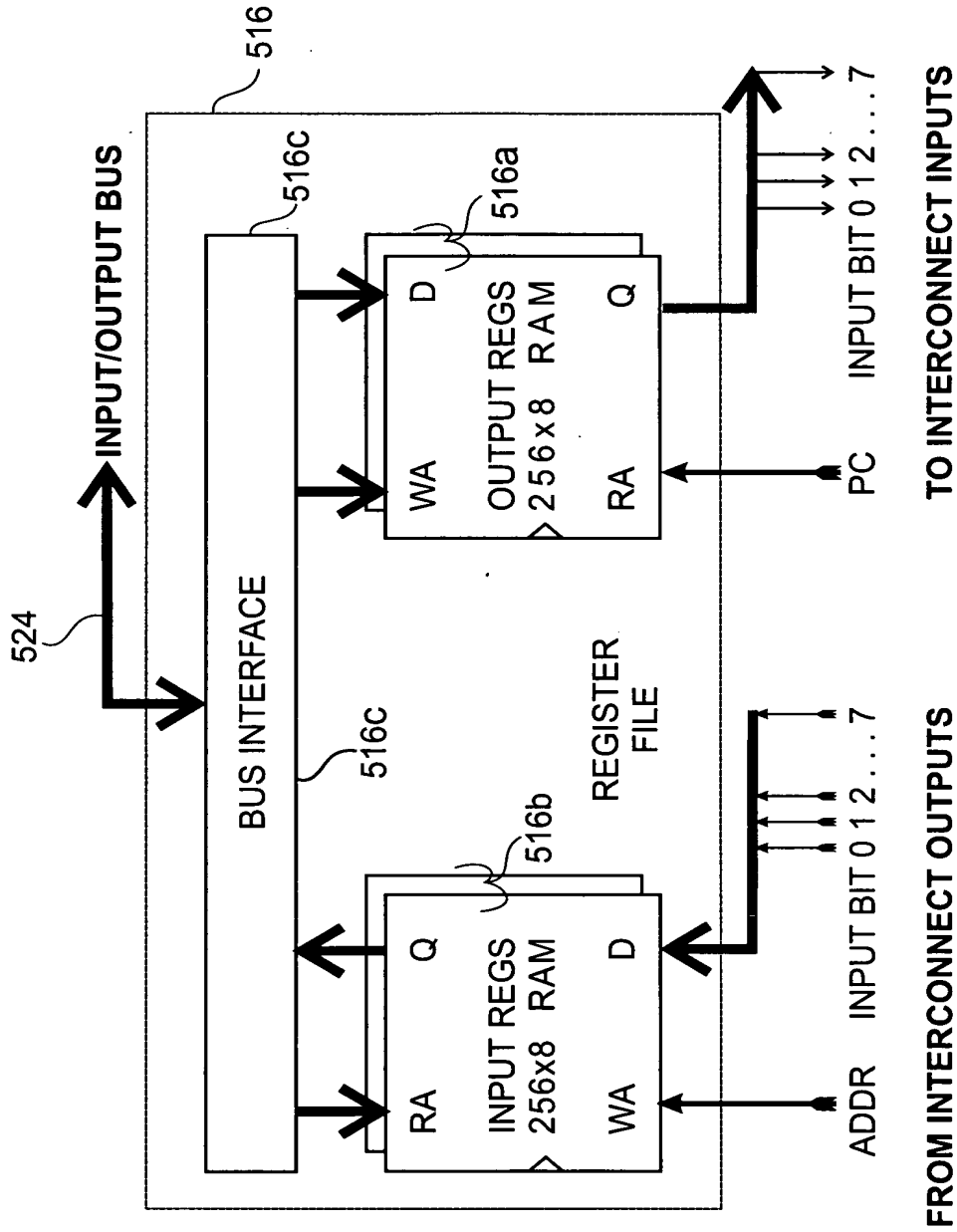


FIG. 6

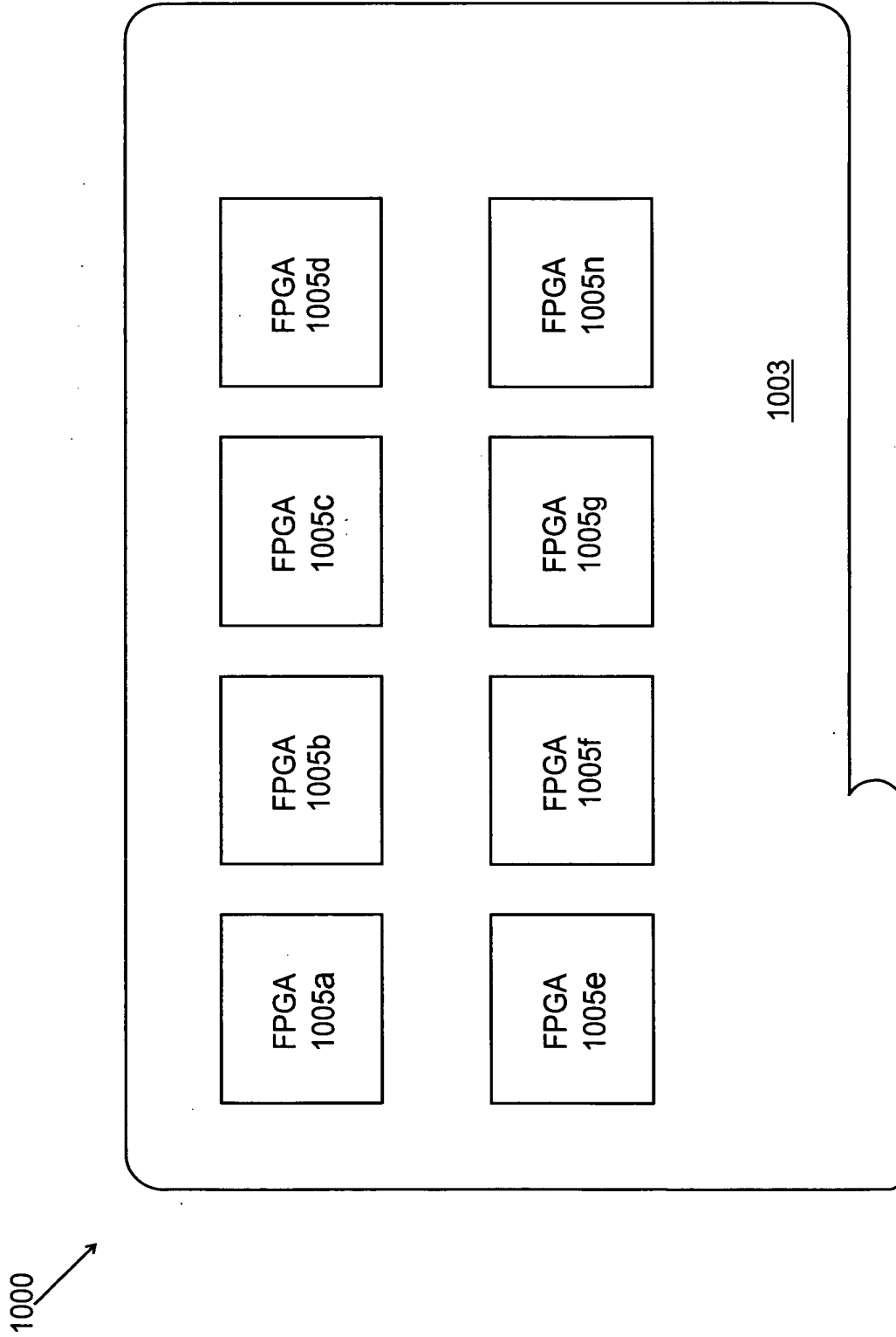


FIG. 7